CS144
An Introduction to Computer Networks

The Link Layer
aka Physical Layer

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Goals for today

1. **Capacity:**
   What determines the maximum data rate of a link?
   ► How can we get close to the maximum capacity?

2. **Clocks:** Two communicating entities cannot have exactly the same clock or frequency. How can they communicate?
The 4 Layer Internet Model

Source End-Host
- Application
- Transport
- Network
- Link

Router
- Network
- Link

Destination End-Host
- Application
- Transport
- Network
- Link
Total time to send a packet across a link: The time from when the first bit is transmitted until the last bit arrives.

\[ t = t_p + t_l = \frac{p}{r} + \frac{l}{c} \]

Example: A 100bit packet takes \(10 + 5 = 15\mu s\) to be sent at 10Mb/s over a 1km link.
Data

IP addresses
Source 171.64.74.55
Destination 176.22.45.66

Example of “On-Off Keying”

Volts
+5V
0V
time

Data

Signal

0 1 1 0 0 1 0 1 1 0 1 0 1 0 1 0

end

start

“176”
Keith: So what was this about?
What determines the data rate?

Q: What determines the steepness (i.e. rate) of this change?
Q: How does the rate of change affect the data rate?
Fiber-optic communication links

Commercial lasers can be switched on/off at rates above 100GHz

Light travels through the fiber because of total internal reflection

Each fiber is smaller than a human hair
Characteristics of fiber-optical links

High bandwidth:
- Lasers can be switched on/off very fast. ("very steep rate of change")
- The signal remains the same for a long distance ("low dispersion or spreading")
- Multiple signals of different colors can be sent over a cable simultaneously ("wavelength division multiplexing" or WDM).

Low signal loss: Typically 0.3dB/km (*)

Electromagnetic interference immunity: An opaque jacket means light cannot enter the cable. Total internal reflection means it is very hard to detect the signal outside the cable.

(*) Power loss (dB) = $10 \log_{10} \frac{P_1}{P_0}$

For example: If power is reduced from 1W to 1mW, we would write: Power loss (dB) = $10 \log_{10} \frac{P_1}{P_0}$ = -30dB, or simply a “30dB loss". 0.3dB/km means about 7% is lost per km.
Fiber-optic communication links

Typically used for very long and/or very fast communications
- Long haul links in the public Internet, e.g. 200km @ 400 Gb/s
- To connect buildings on campus, e.g. 2km @ 100Gb/s
- Between racks of equipment in a data center, e.g. 50m @ 100Gb/s
- World-record NEC (2011) 101.7 Tb/s over 370 WDM channels at 165km!
What determines the maximum data rate of a cable, fiber, wireless link, etc?

Q: What happens if we put the “bits” closer and closer together?
Q: If we can’t put them closer together, how can we increase the number bits of information transmitted per second?
Q: What other factors limit the number of bits per second we can transmit?

Q: Are there any other factors other than “Bandwidth” and “Noise” that determine the maximum data rate of a channel?
Claude Shannon


Shannon’s Juggling Machines  https://www.youtube.com/watch?v=dyC5bgpY86c
Shannon Capacity

• Shannon capacity is the maximum error-free rate we can transmit through a channel (e.g. wire, fiber, air, ...).
• The maximum “data rate”.
• Under some mild assumptions:

\[
\text{Shannon Capacity} = B \log_2 \left( 1 + \frac{S}{N} \right)
\]

• In other words, it depends only on Bandwidth and Signal-to-Noise ratio!
• EE376A: Information Theory. Wow.
Shannon Capacity

**But**: It doesn’t tell us how to achieve the capacity.

**Hence**: Huge numbers of researchers and engineers have spent decade trying to approach it, in different contexts:

- WiFi
- Cellular telephones
- Ethernet
- Optical Fibers
- ADSL Broadband access
- Modems
- ...

Analog signals

Frequency = 1/wavelength
Bandwidth: size of frequency range
Phase: location of peak within the wavelength

On-Off Keying (OOK)
- One frequency
- 2 amplitudes

Figure 19.1 Simple on/off binary keying.
Sending 0s and 1s

Frequency Shift Keying (FSK)

Amplitude Shift Keying (ASK)

Phase Shift Keying (PSK)
- For the same frequency + amplitude, vary the phase
- No variation in power (amplitude) or wavelength (frequency)
Phase in Analog signals

\[ \cos(\pi t) \]

\[ \sin(\pi t) = \cos(\pi t - \frac{\pi}{2}) \]

\[ -\cos(\pi t) = \cos(\pi t - \pi) \]

- Same frequency
- Same amplitude
- Different phase
Phase in Analog signals

- Same frequency
- Same amplitude
- Different phase

\[ \cos(\pi t) \]

\[ \cos(\pi t) = \cos(\pi t - \pi) \]
I/Q constellations

For the same frequency:

- What I/Q constellation (amplitude, phase) should I select?
- How should I assign a symbol (amplitude, phase) = to bits?
Quadrature Phase Shift Keying (QPSK)

1. For each symbol:
   - What is the amplitude?
   - What is the phase?

2. Represent each symbol as a bit (or bits).
Quadrature Amplitude Modulation (16-QAM)

1. How many symbols?
2. How many amplitude variations?
3. How many phase variations?
4. How many bits per symbol?
Example 32 bit word transmission using 16-QAM
Examples today

ASK/OOK: Wired Ethernet
FSK: Bluetooth
BPSK: 802.11 abgn
QPSK: 802.11 abgn, LTE
16-QAM: 802.11abgn, LTE
64-QAM: 802.11 abgn, LTE, 5G
256-QAM: 5G
1024-QAM: Home powerline communication
32768-QAM: ADSL (digital data over long telephone cables)
Clocks
If we don’t know the sender’s (TX) clock

If the RX clock is $p\%$ slower than the TX clock, then: $T_{RX} = T_{TX} \left(1 + \frac{p}{100}\right)$

After $\frac{0.5}{10^{-2}p}$ bit times, the RX clock will miss a bit completely.

Usually expressed in parts per million (ppm) rather than percentage
Synchronous communication on network links

TX clock domain

Flip-Flop

Flip-Flop

Sender’s Clock

Clock Recovery Unit

RX clock domain

Flip-Flop

Flip-Flop

10MHz clock +/- 100ppm

10MHz clock

network link
If the clock is not sent separately, the data stream must have enough transitions for the receiver to determine when to sample the arriving data.
Encoding for clock recovery
Example #1: 10Mb/s Ethernet

Advantages of Manchester encoding:
- Guarantees one transition per bit period.
- Ensures d.c. balance (i.e. equal numbers of hi and lo).

Disadvantages
- Doubles bandwidth needed in the worst case.
IP addresses

<table>
<thead>
<tr>
<th>Source</th>
<th>Destination</th>
</tr>
</thead>
<tbody>
<tr>
<td>171.64.74.55</td>
<td>176.22.45.66</td>
</tr>
</tbody>
</table>

Start of transmission

"176"

Encoded Data: 10110000

Manchester Encoded Data:

<table>
<thead>
<tr>
<th>Data</th>
<th>1</th>
<th>0</th>
<th>1</th>
<th>1</th>
<th>1</th>
<th>0</th>
<th>0</th>
<th>0</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>signal</td>
<td>1</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>1</td>
<td>1</td>
<td>0</td>
</tr>
</tbody>
</table>

Signal pattern:

![Signal pattern graph]

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Encoding for clock recovery

Example #2: 4b/5b encoding

Advantages of 4b/5b encoding:
- More bandwidth efficient (only 25% overhead).
- Allows extra codes to be used for control information.

Disadvantages
- Fewer transitions makes clock recovery a little harder.

<table>
<thead>
<tr>
<th>4-bit data</th>
<th>5-bit code</th>
</tr>
</thead>
<tbody>
<tr>
<td>0000</td>
<td>11110</td>
</tr>
<tr>
<td>0001</td>
<td>01001</td>
</tr>
<tr>
<td>0010</td>
<td>10100</td>
</tr>
<tr>
<td>...</td>
<td>...</td>
</tr>
</tbody>
</table>

Example: 4-bit data: 0000, 0001, 0010... 5-bit code: 11110, 01001, 10100...
Elasticity Buffer

A network link connects the sender's clock domain to the receiver's clock domain. The sender's clock is 10MHz with a tolerance of +/- 100ppm. The receiver's clock is also 10MHz with a tolerance of +/- 100ppm. A Clock Recovery Unit is used to synchronize the clocks between the TX and RX domains.
Sizing an elasticity buffer

\[ R_{rx} > R_{tx} \]

\[ R_{tx} > R_{rx} \]
Sizing an elasticity buffer

1. Hold buffer nominally at $B/2$.
   - At start of new packet, allow buffer to fill to $B/2$.
   - Or, make sure buffer drains to $B/2$ before new packet.
2. Size buffer so that it does not overflow or underflow before packet completes.
3. ($R_{tx} > R_{rx}$): Given inter packet gap, size $B/2$ for no overflow.
4. ($R_{rx} > R_{tx}$): Given max length packet, pick $B/2$ for no underflow.
Preventing overflow

To prevent overflow:

We require: \( P_{\text{max}} - R_{\text{min}} \left( \frac{P_{\text{max}} - B}{2R_{\text{max}}} \right) \leq B \)

\( \left( \frac{R_{\text{max}} - R_{\text{min}}}{R_{\text{max}}} \right) P_{\text{max}} + \frac{B \times R_{\text{min}}}{2R_{\text{max}}} \leq B \). But \( \frac{R_{\text{min}}}{R_{\text{max}}} \approx 1 \).

\[ \therefore \text{require} \left( \frac{R_{\text{max}} - R_{\text{min}}}{R_{\text{max}}} \right) P_{\text{max}} \approx \left( \frac{R_{\text{max}} - R_{\text{min}}}{R} \right) P_{\text{max}} \leq B/2 \]
Preventing underflow

We require:

\[
\frac{P_{\text{max}}}{R_{\text{max}}} + \frac{B}{2R_{\text{min}}} \geq \frac{P_{\text{max}}}{R_{\text{min}}}
\]

To prevent underflow:

\[
\left(\frac{R_{\text{max}} - R_{\text{min}}}{R_{\text{max}}}\right)P_{\text{max}} \approx \left(\frac{R_{\text{max}} - R_{\text{min}}}{R}\right)P_{\text{max}} \leq B/2
\]
Sizing an elasticity buffer

Example

Maximum packet size 4500 bytes

Clock tolerance +/- 100 ppm

\[
\frac{R_{\text{max}}}{R} \div \frac{R_{\text{min}}}{R} = 200 \times 10^6
\]

\[
B = 2(4500 \times 8 \times 200 \times 10^6) = 14 \text{ bits}
\]

Therefore,

1. Elasticity buffer needs to be at least 14 bits
2. Wait for at least 7 bits before draining buffer
3. Inter-packet gap at least 7 bits